[Confidential]

Sony IP&S Inc.   No.   APM18-017

Technical Memo

Class [3]	Issued : July 23, 2018

Subject: Software Release [V1.13 => V1.14]

[Applicable Model]

Model / Destination	Serial Number	Number of Unit
DWM-02N CE7		
DWM-02N CN		
DWM-02N J		
DWM-02N UC		

[Description]

Updated version of the DWM-02N (V1.13 => V1.14) is released.
Upgrade it as necessary.

[Applicable Serial Number]

DWM-02N/B J   30049 or lower
DWM-02N/G J   61247 or lower
DWM-02N/WL J   60154 or lower
DWM-02N/WM J   60242 or lower
DWM-02N/WH J   60323 or lower
DWM-02N/14 UC   10028 or lower
DWM-02N/30 UC   10045 or lower
DWM-02N/42 UC   10081 or lower
DWM-02N/21 CE7   40110 or lower
DWM-02N/33 CE7   40498 or lower
DWM-02N/42 CE7   40294 or lower
DWM-02N/29 CN   50040 or lower

Applicable update versions are the following versions.

Model	Versions
DWM-02N	V1.13 or lower

[Change Point]

<Error Correction>
The communication signal between devices on the board can be heard as the periodical noise when using the low sensitivity microphone.
The communication processing is improved by changing the setting of the FPGA I/O pin.

[Preparation]

<Software File>
File Name: DWM-02N_Version_1_14_Updater.zip
* Obtain it from Software Download site.

<Others>
Personal Computer
OS : Windows 7, Windows 8.1, or Windows 10
USB cable: Type A - Type Micro B (Part No. 1-835-226-11 or 1-836-906-11)

[Installation Procedure]

Refer to the technical memo APM11-015, "Software Upgrade Procedure for DWT-B01, DWT-P01, DWM-01, and DWR-S01D".

[Confirmation After Installation / Adjustment]

- Confirm the version of the unit.
    1. Turn the power of the unit to ON.
    2. Press the MINUS button several times to indicate the VERSION.
    3. Confirm if the version of the unit is 1.14.

- Confirm the version of each device.
    1. Perform the above operations to indicate the VERSION.
    2. Hold the SET button more than 2 seconds.
    3. Press PLUS, MINUS, and PLUS button in series.
    4. Press the MINUS button more than 2 seconds, MAIN CPU VERSION would be indicated.
    5. Indicate the version of each device (MAIN CPU, FPGA, REMOTE, and CH PLAN) by pressing the MINUS button, and confirm if they are the same versions in the columns for V1.14 in [Software History]

[Software History]

		V1.02 (Initial version)	V1.03	V1.04	V1.10	V1.11	V1.12	V1.13
MAIN CPU		131031A	131205A	141007A	150827A	151112A	160301A	161025A
FPGA		130830A	←	140630A	150806A	←	←	←
REMOTE		130805A	←	←	←	←	←	←
CH PLAN	WL (J)	130918A	←	←	←	←	←	←
	WM (J)	130820A	←	←	←	←	←	←
	WH (J)	130820A	←	←	←	←	←	←
	G (J)	130918A	←	←	←	←	←	←
	B(J)	-	-	-	-	-	130321A	←
	U14-25	-	-	-	150226A	←	←	←
	U30-41	-	-	-	150226A	←	←	←
	U42-51	-	-	-	150226A	←	←	←
	CE21-27	-	-	-	150714A	←	←	←
	CE33-38	-	-	-	150226A	←	←	←
	CE42-48	-	-	-	150226A	←	←	←

		V1.14
MAIN CPU		180510A
FPGA		171017A
REMOTE		←
CH PLAN	WL (J)	←
	WM (J)	←
	WH (J)	←
	G (J)	←
	B(J)	←
	U14-25	←
	U30-41	←
	U42-51	←
	CE21-27	←
	CE33-38	←
	CE42-48	←